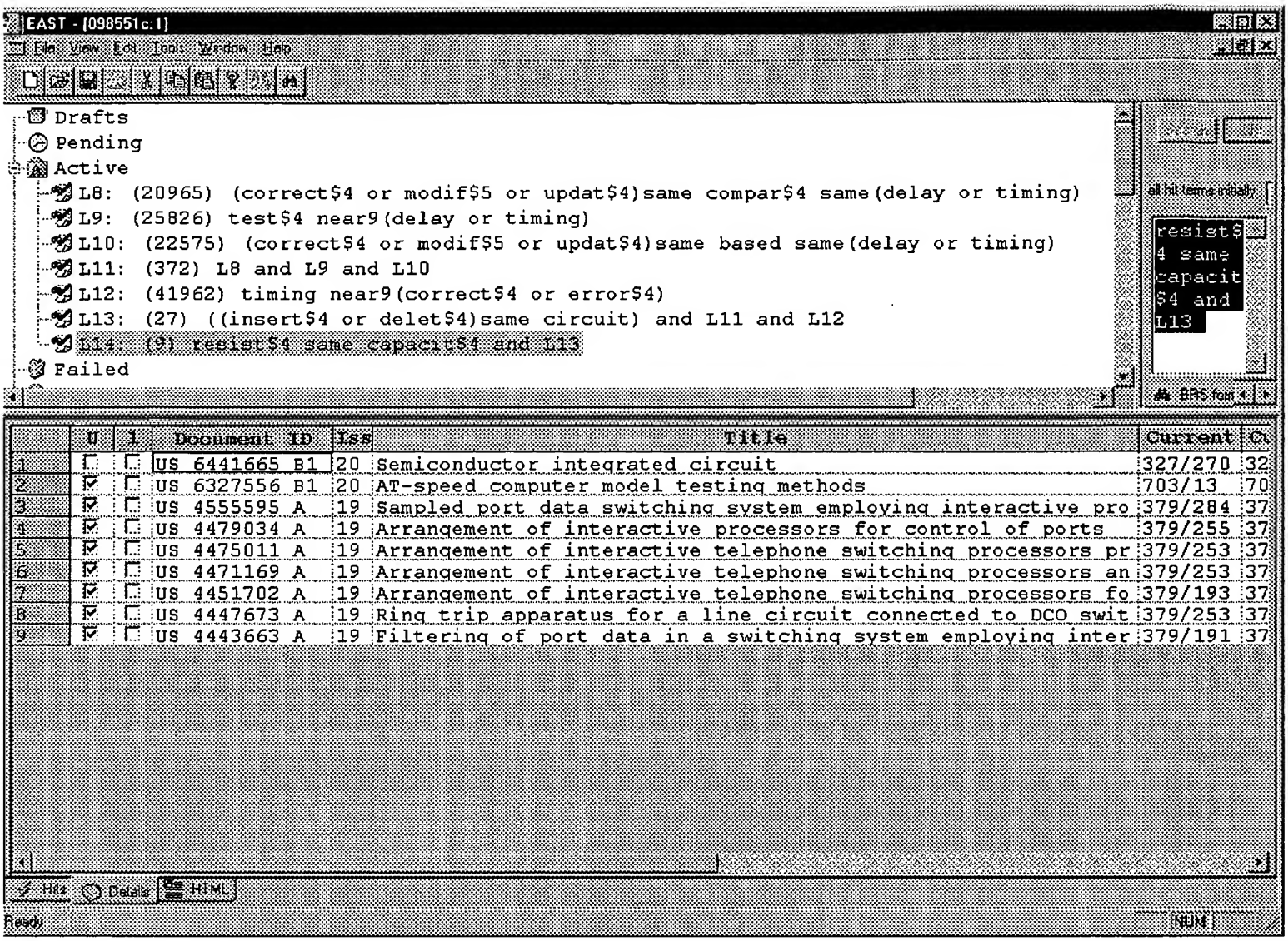
EAST - [098551c:1]
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L8: (20965) (correct$4 or modif$5 or updat$4)same compar$4 same(delay or timing)
L9: (25826) test$4 near9(delay or timing)
L10: (22575) (correct$4 or modif$5 or updat$4)same based same(delay or timing)
L11: (372) L8 and L9 and L10
L12: (41962) timing near9(correct$4 or error$4)
L13: (27) ((insert$4 or delet$4)same circuit) and L11 and L12
L14: (9) resist$4 same capacit$4 and L13
Failed
all hit terms initially
resist$4 same capacit$4 and L13
BRS from
0 1 Document ID Iss Title Current Ct
1 US 6441665 B1 20 Semiconductor integrated circuit 327/270 32
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3 US 4555595 A 19 Sampled port data switching system employing interactive pro 379/284 37
4 US 4479034 A 19 Arrangement of interactive processors for control of ports 379/255 37
5 US 4475011 A 19 Arrangement of interactive telephone switching processors pr 379/253 37
6 US 4471169 A 19 Arrangement of interactive telephone switching processors an 379/253 37
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